

WHAT IS CLAIMED IS:

1. A semiconductor device having a plurality of functional elements formed on a substrate comprising:

5 a slice line for cutting said substrate which is arranged along the outer periphery of said substrate, and a peripheral wire arranged between said slice line and said functional elements.

10 2. The semiconductor device according to claim 1, wherein

said peripheral wire is used for checking the acceptability of the cutting of said substrate.

15 3. The semiconductor device according to claim 1, wherein

said peripheral wire is connected to a constant electric potential.

20 4. The semiconductor device according to claim 3, wherein

said constant electric potential is the ground potential.

5. The semiconductor device according to claim 1,

said peripheral wire has a pad section for checking electric conductivity.

6. The semiconductor device according to claim 1,
wherein

said peripheral wire is connected to drive wires
of said functional elements or signal wires.

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7. The semiconductor device according to claim 1,
wherein

TFT elements and photoelectric converter elements
are arranged as functional elements on said substrate
10 and said peripheral wire is connected to bias wires of
said photoelectric converter elements.

8. The semiconductor device according to claim 6,
wherein

15 TFT elements and photoelectric converter elements
are arranged as functional elements on said substrate
and said peripheral wire is connected to bias wires of
said photoelectric converter elements.

20 9. The semiconductor device according to claim 9,
wherein

said substrate is an insulator.

10. The semiconductor device according to claim

said functional elements carry a wavelength
converter thereon.

11. The semiconductor device according to claim
10, wherein

sub. 11/12/13/14
said wavelength converter is a fluorescent
substance.

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12. A semiconductor device having:

a plurality of functional elements formed on a
substrate; comprising

a peripheral wire arranged along the outer
10 periphery of said substrate and connected to a
reference voltage source.

13. The semiconductor device according to claim
12, wherein

15 said peripheral wire is used for checking the
acceptability of the cutting of said substrate.

14. The semiconductor device according to claim
12, wherein

20 said peripheral wire is held to the ground
potential.

15. The semiconductor device according to claim
12, wherein

checking electric conductivity.

16. The semiconductor device according to claim 12, wherein

said peripheral wire is connected to drive wires of said functional elements or signal wires.

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17. The semiconductor device according to claim 12, wherein

TFT elements and photoelectric converter elements are arranged as functional elements on said substrate and said peripheral wire is connected to bias wires of said photoelectric converter elements.

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18. A semiconductor device according to claim 16, wherein

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TFT elements and photoelectric converter elements are arranged as functional elements on said substrate and said peripheral wire is connected to bias wires of the photoelectric converter elements.

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19. The semiconductor device according to claim 12, wherein

said substrate is an insulator.

20 The semiconductor device according to claim

said functional elements carry a wavelength converter thereon.

21. The semiconductor device according to claim
20, wherein

said wavelength converter is a fluorescent
substance.

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22. A semiconductor device comprising a TFT
substrate having a plurality of pixels of a plurality
of TFT (thin film transistors) provided on the
substrate,

10 wherein drive wires of said TFT are connected by
way of a wire resistance R_s ;

each of the pixels on said TFT substrate comprises
said TFT and a photoelectric converter element, bias
wires of said photoelectric converter elements and
15 drive wires of said TFT being connected;

said TFT substrate has a slice line for cutting
said TFT substrate arranged along the outer periphery
thereof; and

20 a peripheral wire is arranged between said slice
line and said TFT substrate.

23. The semiconductor device according to claim
22, said peripheral wire is used for checking the
acceptability of the cutting of said substrate.

24. The semiconductor device according to claim
22, wherein

said peripheral wire is connected to a constant voltage source.

25. The semiconductor device according to claim
5 22, wherein

said peripheral wire is held to the ground potential.

26. The semiconductor device according to claim
10 22, wherein

said peripheral wire has a pad section for checking electric conductivity.

27. The semiconductor device according to claim
15 22, wherein

said peripheral wire is connected to said TFT drive wires or signal wires.

28. The semiconductor device according to claim
20 22, wherein

said peripheral wire is connected to bias wires of said photoelectric converter elements.

29. A semiconductor device according to claim 27,

said peripheral wire is connected to the bias wires of said photoelectric converter elements.

30. The semiconductor device according to claim
22, wherein

said wire resistance R_s is expressed by $R_s >$
100 R_o ,

5 where R_o is the resistance between a TFT driver
and a TFT drive terminal.

31. A radiation detection apparatus comprising:
a radiation source, and

10 a TFT substrate having a plurality of pixels of a
plurality of TFT (thin film transistors) provided on
the substrate,

wherein

each of the pixels on said TFT substrate comprises
15 said TFT and a photoelectric converter element, bias
wires of said photoelectric converter elements and
drive wires of said TFT being connected;

said TFT substrate has a slice line for cutting
said TFT substrate arranged along the outer periphery
20 thereof; and

a peripheral wire is arranged between said slice
line and said TFT substrate.

32. The radiation detection apparatus according

33. A radiation detection system having the

radiation detection apparatus according to claim 31 or
32 comprising:

signal processing means for processing signals
from said radiation detection apparatus;

5 recording means for recording signals from said
signal processing means;

display means for displaying the signals from said
signal processing means; and

transmission means for transmitting the signals
10 from said signal processing means.

34. A method of manufacturing the semiconductor
device according to claim 22 comprising the steps of:

cutting said substrate to predetermined dimensions
15 along said slice lines;

examining the electric conductivity of said
peripheral wire; and

mounting said TFT driver and said photoelectric
converter element driver after examining the electric
20 conductivity of said peripheral wire.

35. The method according to claim 34, further
comprising a step of:

connecting said peripheral wire to said TFT drive
elements.

36. The method according to claim 34, further comprising a step of:

cutting the connection of said peripheral wire and said TFT drive wires or said bias wires of said
5 photoelectric converter elements at the connecting portion thereof after examining the electric conductivity of said peripheral wire.

37. The method according to claim 34, further
10 comprising a step of:

bonding a plurality of substrates obtained after the cutting step.